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INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Use as many sheets as necessary)</i>				Application Number	10/568,666-Conf. #5116
				Filing Date	January 21, 2008
				First Named Inventor	Tomoyoshi Sato
				Art Unit	2123
				Examiner Name	
Sheet	1	of	1	Attorney Docket Number	29898/40913

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			

FOREIGN PATENT DOCUMENTS						
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NON PATENT LITERATURE DOCUMENTS			
Examiner Initials	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		European Search Report for EP 04 77 2336 dated 11/11/2008.	
		BONDALAPATI et al., "Reconfigurable Computing Systems," <i>Proceedings of the IEEE</i> , 90:1201-1208 (2002).	
		BORGATTI et al., "A Reconfigurable System Featuring Dynamically Extensible Embedded Microprocessor, FPGA, and Customizable I/O," <i>J. of Solid-State Circuits</i> , 38:521-529 (2003).	
		HAUCK et al., "Reconfigurable Computing: A Survey of Systems and Software," <i>ACM Computing Surveys</i> , 34:171-210 (2002).	
		HAUCK, "The Roles of FPGA's in Reprogrammable Systems," <i>Proceedings of the IEEE</i> , 86:615-638 (1998).	
		MERIBOUT, "A New Scalable and Reconfigurable Architecture," <i>IEEE Potentials</i> , 22:26-32 (2003).	
		SUGAWARA et al., "Dynamically Reconfigurable Processor Implemented with IPFlex's DAPDNA Technology," <i>IEICE Trans. Inf. & Syst</i> , E87-D: 1997-2003 (2004).	

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